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1/2 ページ

3

## PATENT ABSTRACTS OF JAPAN

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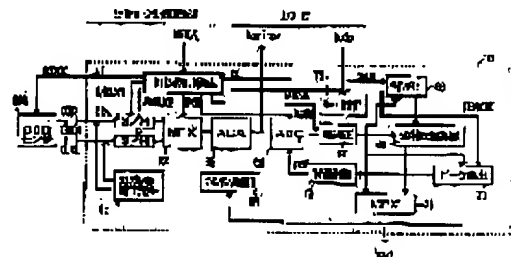
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## (54) SIGNAL PROCESSING UNIT

## (57)Abstract:

**PURPOSE:** To provide the signal processing unit in which the manufacture cost increase and the design cost increase in the processing unit are not incurred by providing an LSI automating a preliminary processing of an output signal of a CCD line sensor at a high speed accurately and suppressing the control variation between units to a minimum in regard of the signal processing unit having a means reading a picture of an original through the use of the CCD line sensor.

**CONSTITUTION:** The signal processing unit is provided with a one-chip integrated circuit element 10 that integrates a clamp means 61 clamping an output signal from a valid picture part of a CCD line sensor 228, a sample-and-hold means 62 sampling and holding an output signal of the clamp means 61, a variable gain amplifier means 65 controlling a gain of an output signal of the sample-and-hold means 62, a means 67 applying dark correction to an output signal of the variable gain amplifier means 62, a digital conversion means 66 converting an output signal of the dark level correction means 67 into a digital signal, a means 69 applying distortion correction to an output signal of the digital conversion means 66, a means 70 detecting a peak level of an output signal of the digital conversion means 66 and a means 64 applying gain adjustment control to the variable gain amplifier 65 based on the peak level into one chip.



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